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Abstract of the Disclosure

A fractional multi-modulus prescaler is disclosed wherein the output of the VCO is separated into four signals 90 degrees phase-related to one another. The phase signals are selected by a multiplexer for application of a division function during the modulus time period. The multiplexer is returned to the original selected phase prior to responding to a subsequent phase select control signal, whereby the generation of multi-modulus spurious frequency signals is prevented.